



INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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First Named Inventor	Sherif Ahmed Abdel-Wahab Hammouda
Art Unit	Not yet assigned 2825
Examiner Name	Not yet assigned Lin, Sun J.

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee
/JSL/		2005/028113 A1	February 3, 2005	Lin Yiqun et al.

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Cite No. (optional)	Country	Number	Publication Date	Name of Applicant or Patentee

OTHER DOCUMENTS

Examiner's Initials*	Cite No. (optional)	
/JSL/		Funaba et al., "A Fast and Accurate Method of Redesigning Analog Subcircuits for Technology Scaling," <i>1998 Design Automation Conference</i> , pp. 489-494 (February 10, 1998).
		Hammouda et al., "Analog IP Migration Using Design Knowledge Extraction," <i>IEEE 2004 Custom Integrated Circuits Conference</i> , pp. 333-336 (October 3, 2004).
		Phelps et al., "A Case Study of Synthesis for Industrial-Scale Analog IP: Redesign of the Equalizer/Filter Frontend for an ADSL Codec," <i>37th Annual Design Automation Conference</i> , pp. 1-6 (June 5, 2000).
		Xu Jinghan et al., "IC Design Automation from Circuit Level Optimization to Retargetable Layout," <i>Electronics, Circuits and Systems, ICECS 2001</i> , pp. 95-98 (September 2, 2001).
/JSL/		International Search Report and Written Opinion for International Application No. PCT/EP2005/054928 (September 29, 2005).

EXAMINER SIGNATURE: /Sun J. Lin/	DATE CONSIDERED: 05/11/2009
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